Low Inductance Capacitors (SnPb)



0612/0508/0306 Tin Lead Termination "B"

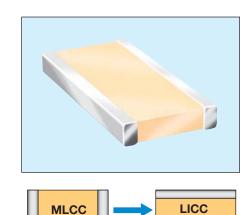
GENERAL DESCRIPTION

The key physical characteristic determining equivalent series inductance (ESL) of a capacitor is the size of the current loop it creates. The smaller the current loop, the lower the ESL.

A standard surface mount MLCC is rectangular in shape with electrical terminations on its shorter sides. A Low Inductance Chip Capacitor (LICC) sometimes referred to as Reverse Geometry Capacitor (RGC) has its terminations on the longer sides of its rectangular shape. The image on the right shows the termination differences between an MLCC and an LICC.

When the distance between terminations is reduced, the size of the current loop is reduced. Since the size of the current loop is the primary driver of inductance, an 0306 with a smaller current loop has significantly lower ESL then an 0603. The reduction in ESL varies by EIA size, however, ESL is typically reduced 60% or more with an LICC versus a standard MLCC.

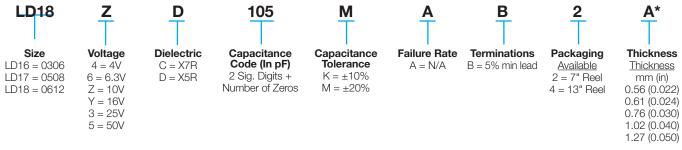
AVX LICC products are available with a lead termination for high reliability military and aerospace applications that must avoid tin whisker reliability issues.



PERFORMANCE CHARACTERISTICS

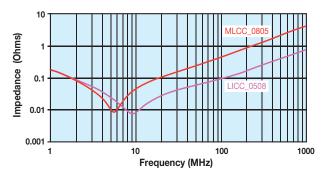
Capacitance Tolerances	K = ±10%; M = ±20%
Operation	X7R = -55°C to +125°C
Temperature Range	X5R = -55°C to +85°C
	X7S = -55°C to +125°C
Temperature Coefficient	X7R, X5R = ±15%; X7S = ±22%
Voltage Ratings	4, 6.3, 10, 16, 25 VDC
Dissipation Factor	4V, 6.3V = 6.5% max; 10V = 5.0% max; 16V = 3.5% max; 25V = 3.0% max
Insulation Resistance (@+25°C, RVDC)	100,000M Ω min, or 1,000M Ω per μF min.,whichever is less

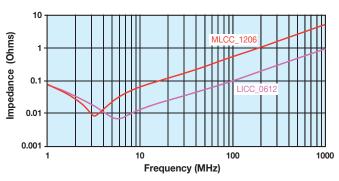
HOW TO ORDER



NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers.

TYPICAL IMPEDANCE CHARACTERISTICS







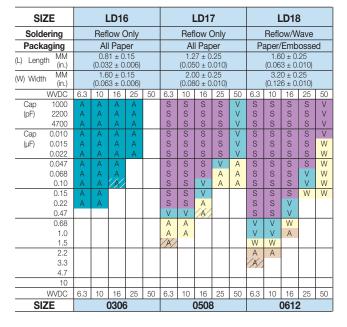
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mm (in)

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PREFERRED SIZES ARE SHADED

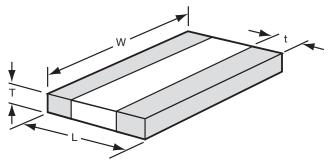


Solid = X7R

= X5R

LD16 - 0306		LD17 - 0508			LD18 - 0612	
Code	Thickness	Code	Thickness	0	Code	Thickness
Α	0.61 (0.024)	S	0.56 (0.022)		S	0.56 (0.022)
		V	0.76 (0.030)		۷	0.76 (0.030)
		Α	1.02 (0.040)		W	1.02 (0.040)
					Α	1.27 (0.050)

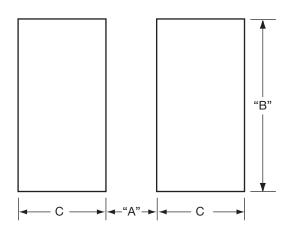
PHYSICAL DIMENSIONS AND PAD LAYOUT



PHYSICAL CHIP DIMENSIONS

	L	W	t
0612	1.60 ± 0.25	3.20 ± 0.25	0.13 min.
	(0.063 ± 0.010)	(0.126 ± 0.010)	(0.005 min.)
0508	1.27 ± 0.25	2.00 ± 0.25	0.13 min.
	(0.050 ± 0.010)	(0.080 ± 0.010)	(0.005 min.)
0306	0.81 ± 0.15	1.60 ± 0.15	0.13 min.
	(0.032 ± 0.006)	(0.063 ± 0.006)	(0.005 min.)

T - See Range Chart for Thickness and Codes



PAD LAYOUT DIMENSIONS mm (in)

	Α	В	C
0612	0.76 (0.030)	3.05 (0.120)	.635 (0.025)
0508	0.51 (0.020)	2.03 (0.080)	0.51 (0.020)
0306	0.31 (0.012)	1.52 (0.060)	0.51 (0.020)