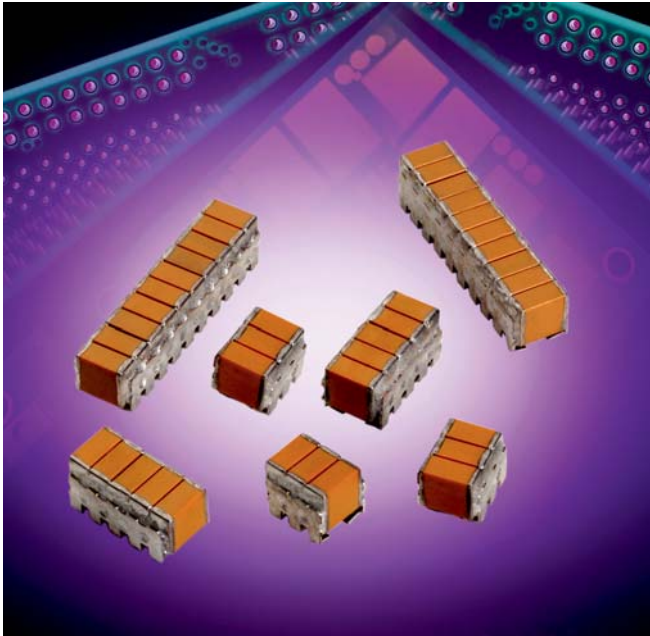


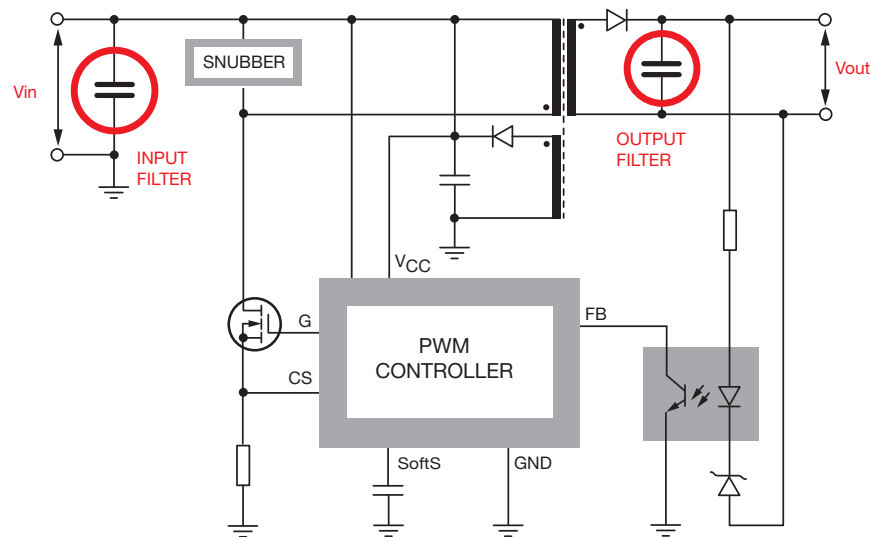
TurboCap

High-CV SMPS Capacitors



The TurboCap, MLC capacitors from AVX Corporation are characterized with very high capacitance in a small volume. By vertical stacking of the ceramic elements, the footprint required for mounting the capacitors is greatly reduced. TurboCaps are ideally suited as filters in the input and output stages of switch mode power supplies (SMPS). With their ultra-low ESR, these capacitors are designed to handle high ripple current at high frequencies and high power levels. The DIP leads in either thru-hole or surface mount configurations offer superior stress relief to the ceramic elements. The leads effectively decouple the parts from the board and minimize thermally or mechanically induced stresses encountered during assembly, temperature cycling or other environmental conditions.

TYPICAL APPLICATION OF TURBOCAP SMPS CAPACITORS FOR INPUT AND OUTPUT FILTERS IN DC/DC CONVERTERS



ELECTRICAL SPECIFICATIONS

Temperature Coefficient

Temperature Coefficient $\pm 15\%$, -55° to $+125^\circ\text{C}$

Capacitance Test (MIL-STD-202 Method 305)

25°C , 1.0 ± 0.2 Vrms (open circuit voltage) at 1KHz

Dissipation Factor 25°C

2.5% Max @ 25°C , 1.0 ± 0.2 Vrms (open circuit voltage) at 1KHz

Insulation Resistance 25°C (MIL-STD-202 Method 302)

500 M Ω - μF , whichever is less.

Insulation Resistance 125°C (MIL-STD-202 Method 302)

50 M Ω - μF , whichever is less.

Dielectric Withstanding Voltage 25°C (Flash Test)

250% rated voltage for 5 seconds with 50 mA max charging current.

Life Test (1000 hrs)

X7R: 200% rated voltage at $+125^\circ\text{C}$.

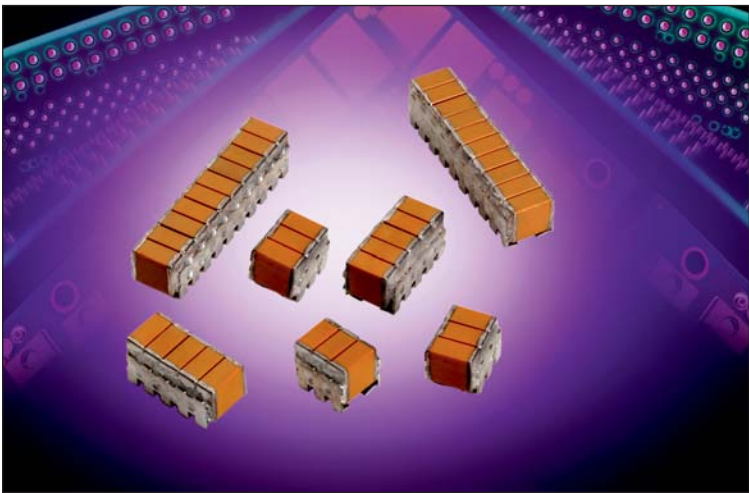
Moisture Resistance (MIL-STD-202 Method 106)

Ten cycles with no voltage applied.

Thermal Shock (MIL-STD-202 Method 107, Condition A)

Immersion Cycling (MIL-STD-202 Method 104, Condition B)

Resistance To Solder Heat (MIL-STD-202, Method 210, Condition B, for 20 seconds)



Typical ESR Performance (Ω)

	27 μF	47 μF	100 μF
ESR @ 10KHz	0.007	0.004	0.003
ESR @ 50KHz	0.003	0.002	0.0015
ESR @ 100KHz	0.002	0.0015	0.001

HOW TO ORDER

AVX Styles: ST12 and ST20

ST12	5	C	186	M	A	N	03
AVX Style ST12 ST20	Voltage 50V = 5	Temperature Coefficient X7R = C	Capacitance Code (2 significant digits + no. of zeros) 1 μF = 105 10 μF = 106 100 μF = 107	Capacitance Tolerance M = $\pm 20\%$	Test Level A = Standard B = Hi-Rel*	Termination N = Straight Lead J = Leads formed in L = Leads formed out	Number of Leads Per Side 03 = 3 05 = 5 10 = 10

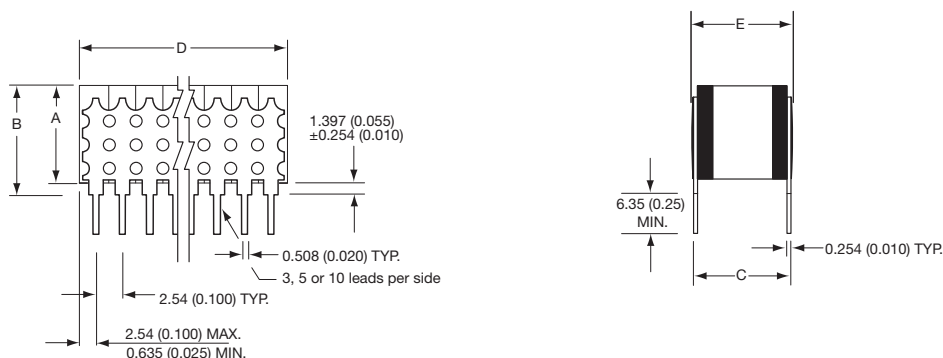
*Hi-Rel screening consists of 100% Group A (B Level), Subgroup 1 per MIL-PRF-49470.

CAPACITANCE (μF)

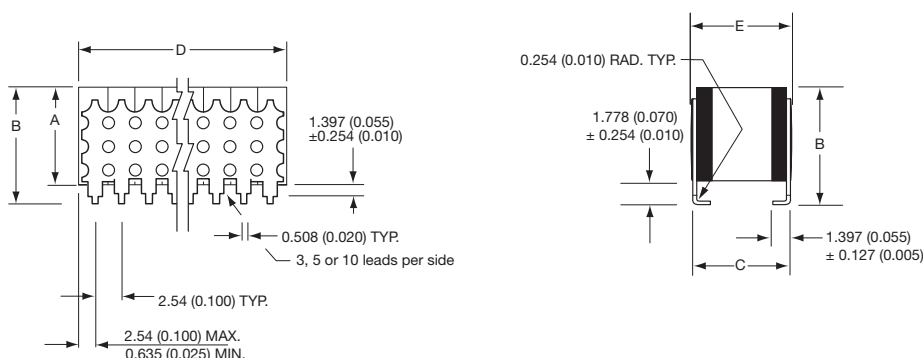
	No. of Leads Per Side		
Style	3	5	10
ST12	18	27	50
ST20	27	47	100

TurboCap

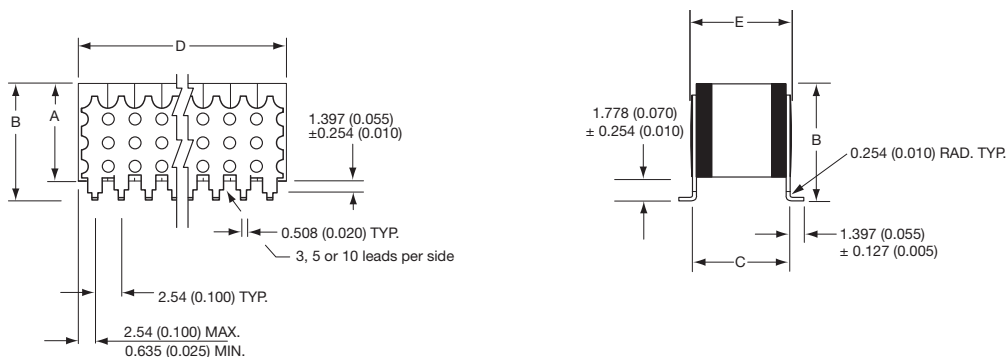
High-CV SMPS Capacitors



“N” STYLE LEADS



“J” STYLE LEADS



“L” STYLE LEADS

DIMENSIONS

millimeters (inches)

Style	A (max.)	B (max.)*	C ± 0.635 (± 0.025)	D ± 1.27 (± 0.050)	E (max.)	Lead Style	No. of Leads Per Side
ST125C***M*N03	3.56 (0.140)	5.21 (0.205)	5.08 (0.200)	8.26 (0.325)	6.35 (0.250)	N	03
ST125C***M*N05	3.56 (0.140)	5.21 (0.205)	5.08 (0.200)	13.34 (0.525)	6.35 (0.250)	N	05
ST125C***M*N10	3.56 (0.140)	5.21 (0.205)	5.08 (0.200)	26.04 (1.025)	6.35 (0.250)	N	10
ST205C***M*N03	5.59 (0.220)	7.24 (0.285)	6.35 (0.250)	8.26 (0.325)	7.62 (0.300)	N	03
ST205C***M*N05	5.59 (0.220)	7.24 (0.285)	6.35 (0.250)	13.34 (0.525)	7.62 (0.300)	N	05
ST205C***M*N10	5.59 (0.220)	7.24 (0.285)	6.35 (0.250)	26.04 (1.025)	7.62 (0.300)	N	10

*The “B” dimension is defined for the “N” Style leads. The “L” and “J” Style Leads are 0.381 (0.015) longer. The ST12 will be 5.89 (0.220), the ST20 will be 7.62 (0.300).

