

REV. Status

PRELIMINARY
04/03/01 YS

PRELIMINARY 1
CHANGED A.11
04/18/01 YS

HDSL2 transformer for use with Level One HDSL2 IC chip set SK70740

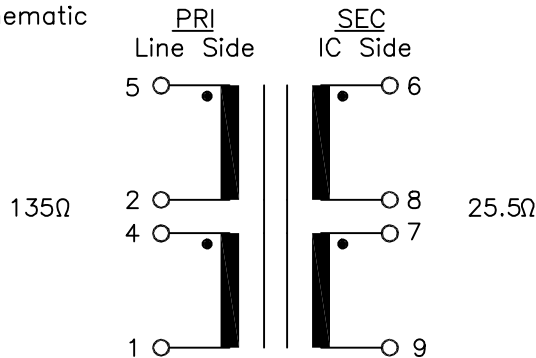
A. Electrical Specifications (@ 25°C)

1. Primary (Line side) Impedance; 135Ω
2. Secondary (IC side) Impedance; 25.5Ω
3. DC Current; 100mA
4. Longitudinal Balance; 55dB MIN @ 20KHz to 560KHz, 0dBm
5. Primary Open Circuit Inductance; 2mH ±10% @ 10KHz, 1Vrms, DC 100mA Measured (1-5) with 2 and 4 shorted
6. Leakage Inductance; 35μH MAX @ 100KHz, 0.1Vrms Measured (1-5) with 2 & 4, 7 & 8, and 6 & 9 shorted
7. Winding Capacitance; 60pF MAX @ 100KHz, 0.1Vrms Measured (5-6) with 2 & 4, 7 & 8 shorted
8. DC Resistance;
 - 1.86Ω MAX measured (1-4)
 - 1.86Ω MAX measured (2-5)
 - 0.90Ω MAX measured (6-9) with (7-8) shorted
9. Turns Ratio; (5-1):(6-9)=2.3:1±2%
10. Dielectric Strength; 1500Vrms 60 seconds @ Pri to Sec or 1875Vrms 1 second
11. Total Harmonic Distortion; -80dB TYP @ 20KHz, 0.7Vrms
12. Return Loss; 15dB MIN, 50KHz to 200KHz

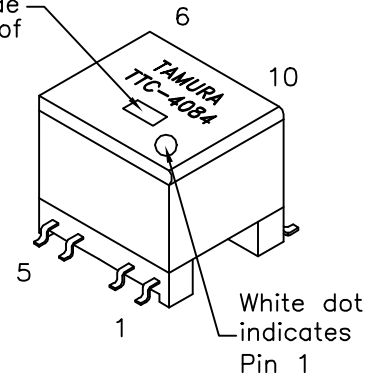
B. Marking; TTC-4084, TAMURA, date code and country of origin

C. Safety: Complies with UL1950 3rd Edition and EN60950

D. Schematic



Date Code
Country of origin

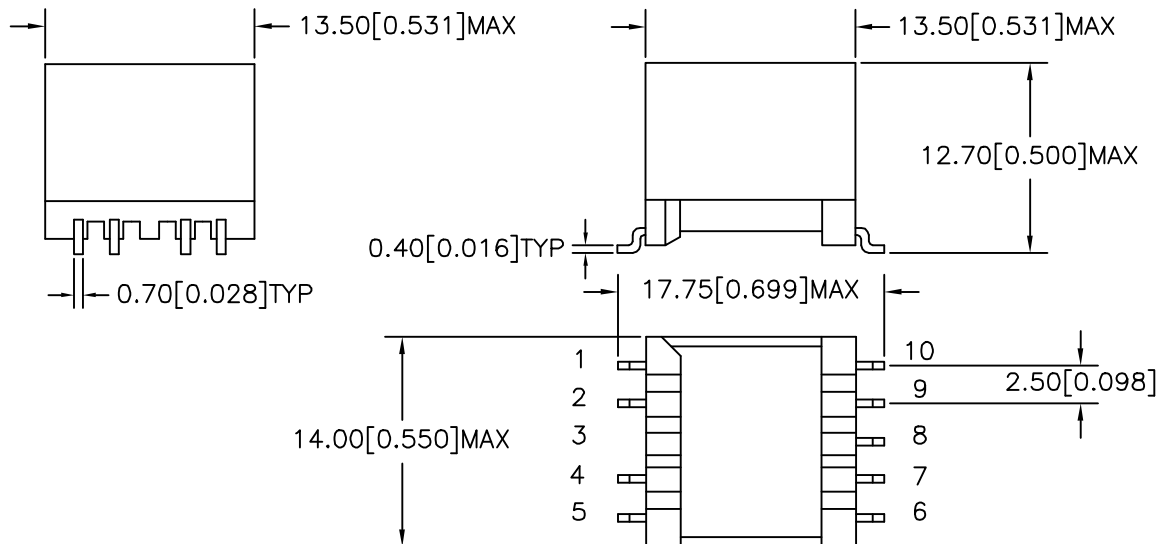


MODEL NUMBER

TTC-4084

E. Mechanical Specifications

Dimensions without tolerances are for reference only.



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APPROVED:

J. Coleman

DWG CONTROL NO.
P-A1-12755
ACAD\TTC\A1127551.DWG

REV
PR1

HDSL2
TRANSFORMER

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TTC-4084

MODEL SPECIFICATION

DIM: mm(in) SCL: 2/1 SH: 1 OF 1

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