

REV. Status

ADSL Transformer designed for use with Texas Instruments AC5 chipset (CO Side)

PRELIMINARY
07/19/01 YS

PRELIMINARY 1
ADDED
PHASE DOTS
07/20/01 YS

PRELIMINARY 2
CHANGED A.7
TO 18pF
09/17/01 YS

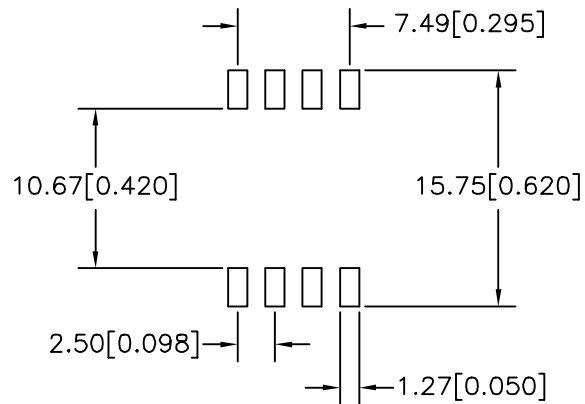
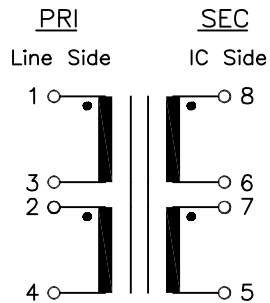
A. Electrical Specifications (@ 25°C)

1. Primary (Line side) Impedance; 100Ω
2. Secondary (IC side) Impedance; 27.5Ω
3. Longitudinal Balance; 60dBm MIN @ 100KHz
4. Total Harmonic Distortion; -80dB MAX @ 100KHz
5. Primary Open Circuit Inductance; 400μH ±5% @ 100KHz, 0.1Vrms Measured (1-4) with 2 and 3 shorted
6. Leakage Inductance; 7.5μH MAX @ 100KHz, 0.1Vrms Measured (1-4) with 2 & 3, 6 & 7, and 5 & 8 shorted
7. Interwinding Capacitance; 18pF MAX @ 100KHz, 0.1Vrms Measured (1-8) with 2 & 3, 1 & 4, 6 & 7 and 5 & 8 shorted
8. DC Resistance; (1-4)=1Ω MAX, with 2 and 3 shorted
(5-8)=1Ω MAX, with 6 & 7 shorted
9. Turns Ratio; (1-4):(8-5)=1.9:1 ±2%
2 & 3, 6 & 7 shorted
10. Dielectric Strength; 1500Vrms 1 minute @ Pri to Sec

B. Marking; TTC-4101, TAMURA, date code and country of origin

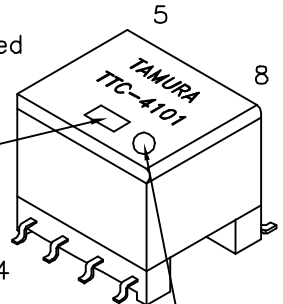
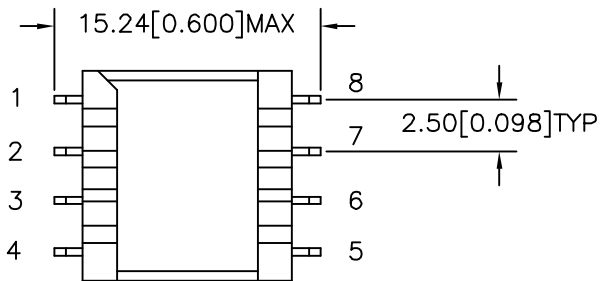
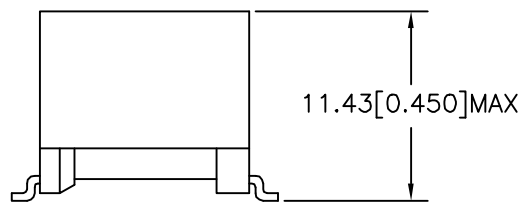
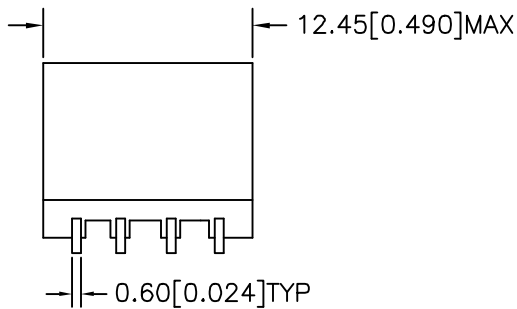
C. Safety; Complies with UL1950 3rd Edition, EN60950

D. Schematic and Mounting Footprint;



E. Mechanical Specifications;

Unless otherwise specified, all tolerances are ±0.25[0.010]



Date Code

MODEL NUMBER

TTC-4101

White dot indicates Pin 1

PREPARED BY:

D. Rund

ENGINEER:

Y. Sekiguchi

QUALITY CONTROL:

D. Kelley

APPROVED:

J. Coleman

DWG CONTROL NO. P-A1-12864
ACAD\TTC\A1128641.DWG

REV PR2

ADSL TRANSFORMER

TTC-4101

MODEL SPECIFICATION

CONTENTS OF THIS DRAWING ARE SUBJECT TO CHANGE WITHOUT PRIOR NOTICE

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DIM: mm(In) SCL: 2/1 SH: 1 OF 1

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